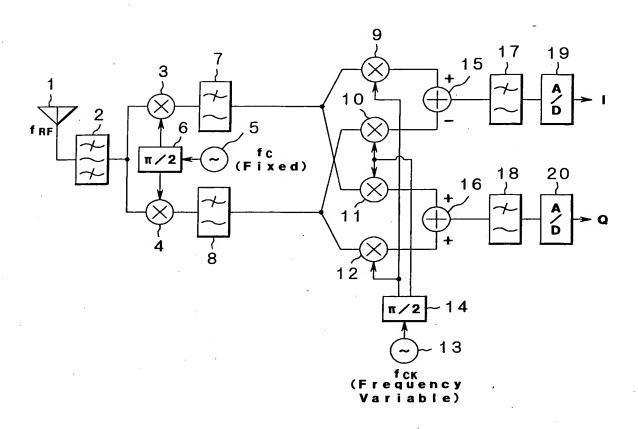
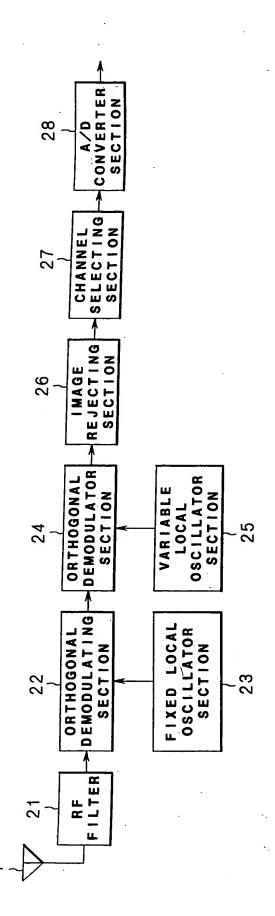
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 1 OF 41

FIG.1A (RELATED ART)



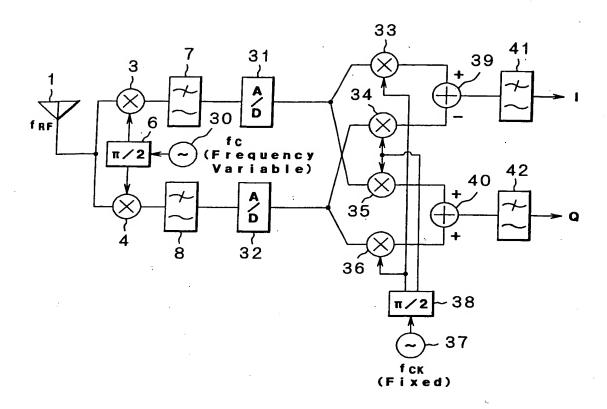
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 2 OF 41

FIG.1B (RELATED ART)



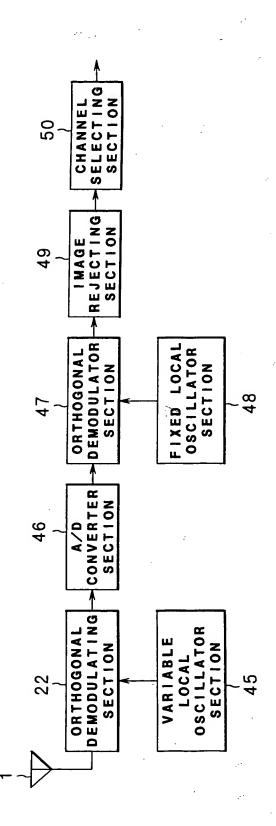
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 3 OF 41

FIG.2A (RELATED ART)



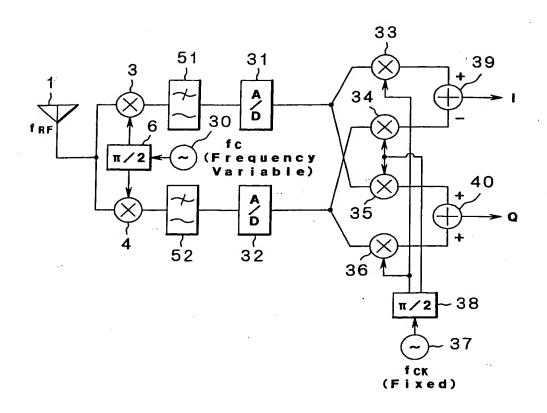
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 4 OF41

FIG.2B (RELATED ART)



OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET <u>5</u> OF <u>41</u>

FIG.3A (RELATED ART)



OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET <u>6</u> OF <u>41</u>

FIG.3B (RELATED ART)

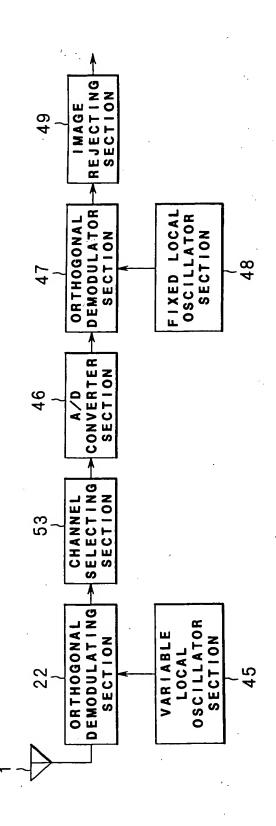
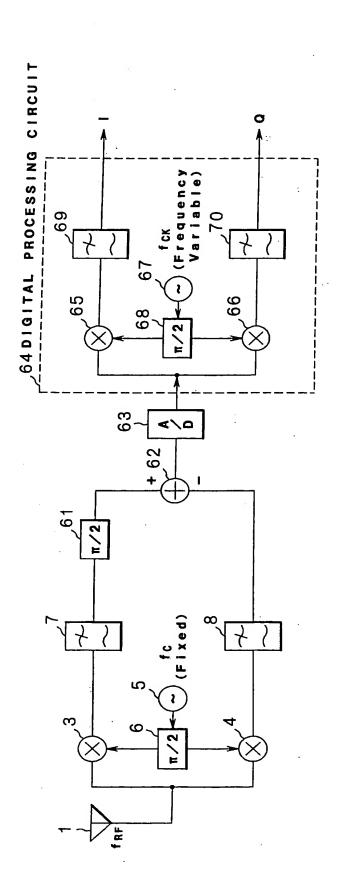


FIG.4 (RELATED ART)



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FIG.5A (RELATED ART)

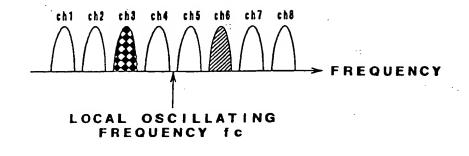
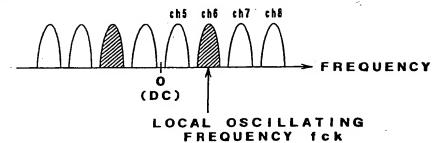


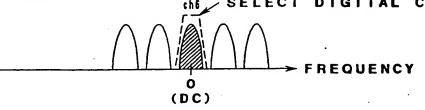
FIG.5B
(RELATED ART)



DIGITAL ORTHOGONAL DEMODULATION

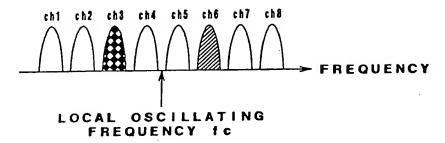
ANALOG ORTHOGONAL

FIG.5C (RELATED ART)



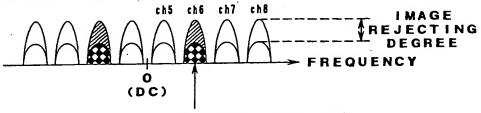
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 9 OF 41

FIG.6A (RELATED ART)



DEMODULATION

FIG.6B (RELATED ART)



LOCAL OSCILLATING FREQUENCY fck

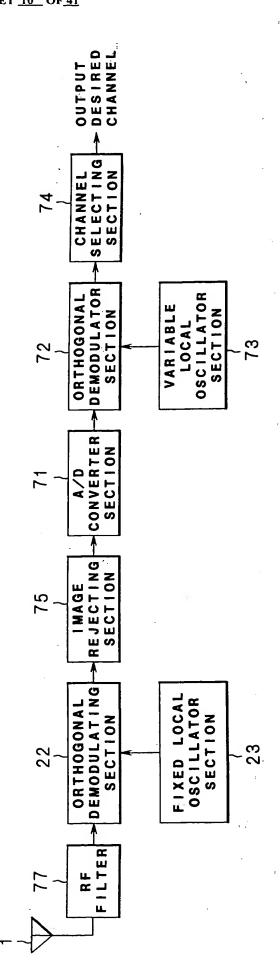


FIG.6C (RELATED ART)

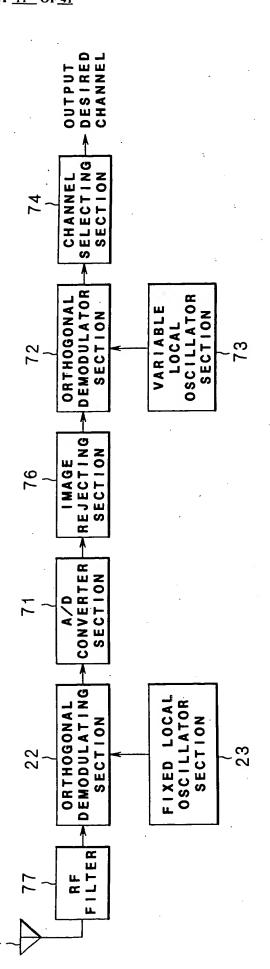
select DIGITAL CHANNEL

(DC)

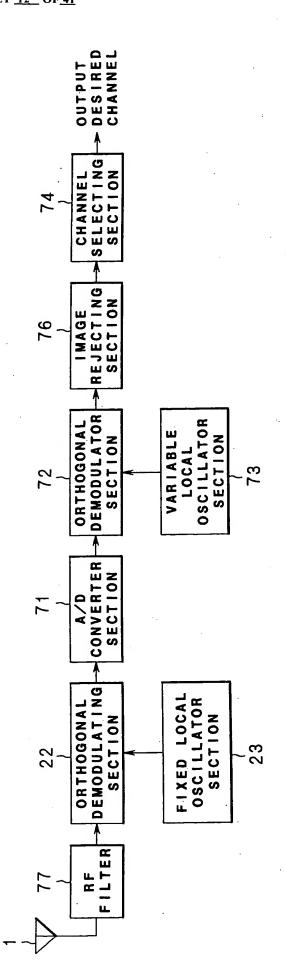




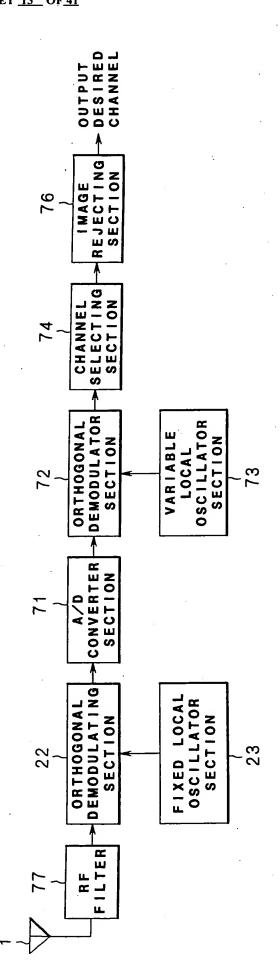


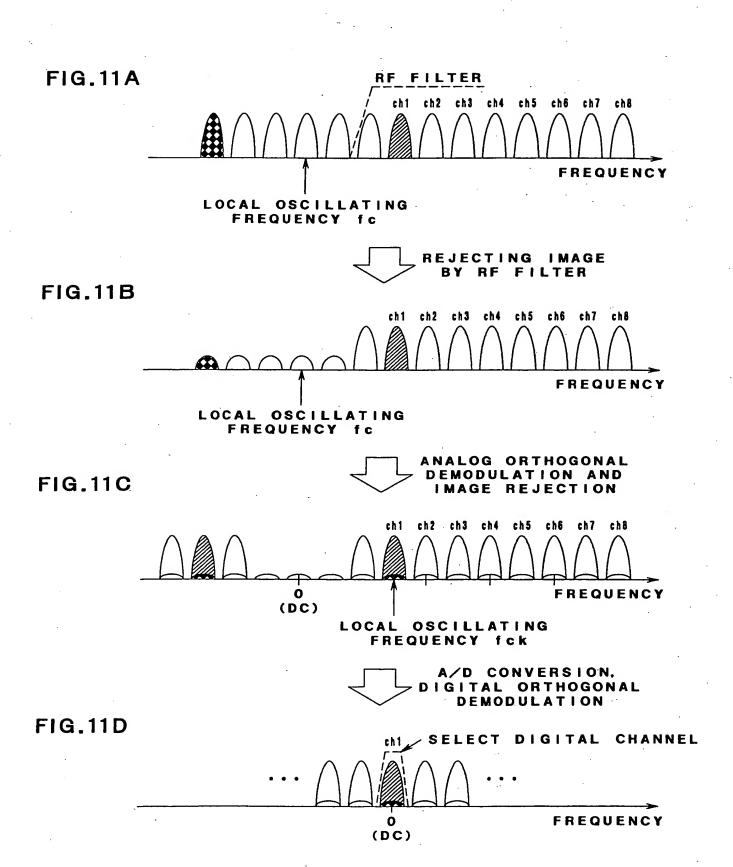




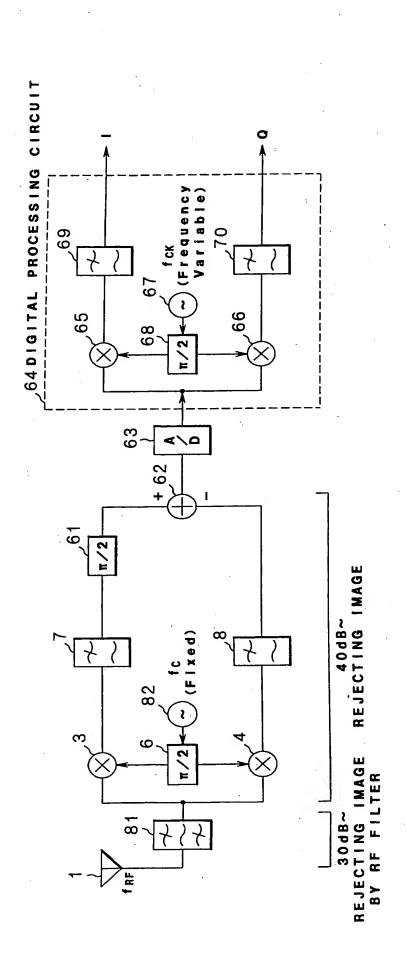






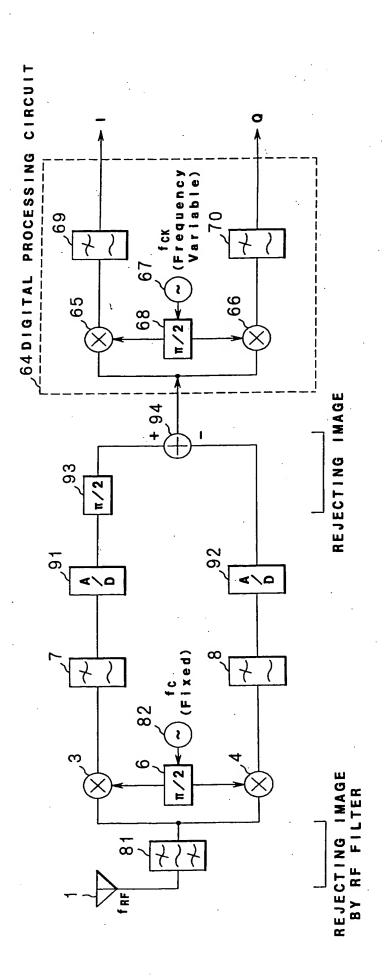




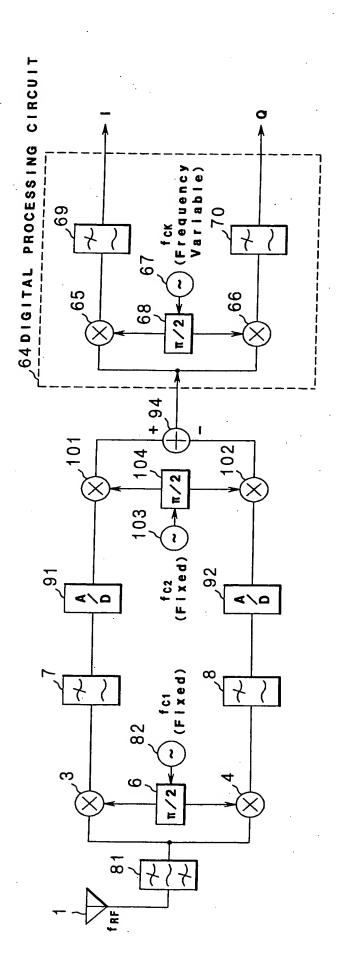


OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 16 OF 41 64 DIGITAL PROCESSING CIRCUIT (Frequency Variable) 69 **67** 99 65 89 4/0 62 + 98 <u></u> α 87 82



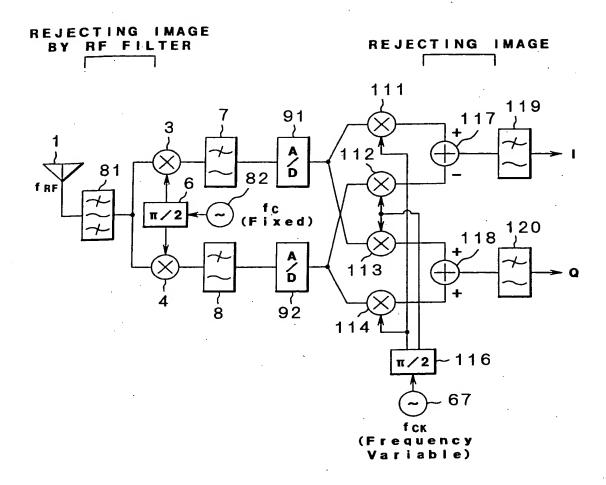






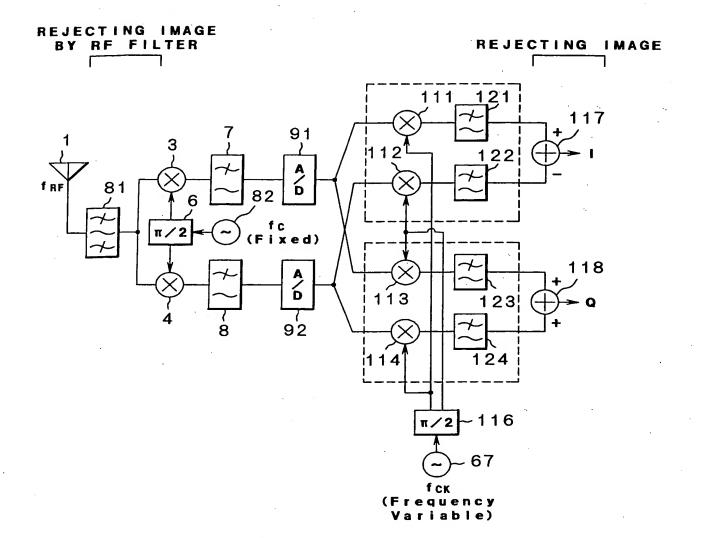
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 19 OF 41

FIG.16



OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 20 OF 41

FIG.17



OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET <u>21</u> OF <u>41</u> CIRCUIT PROCESSING 69 **67** 64 DIGITAL 99 65 89 REJECTING IMAGE 62 61 ဗ္ဗ REJECTING BY RF FI OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 22 OF 41

FIG.19

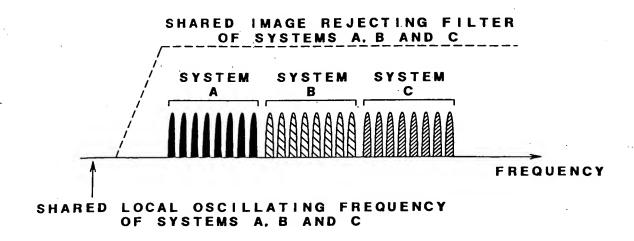
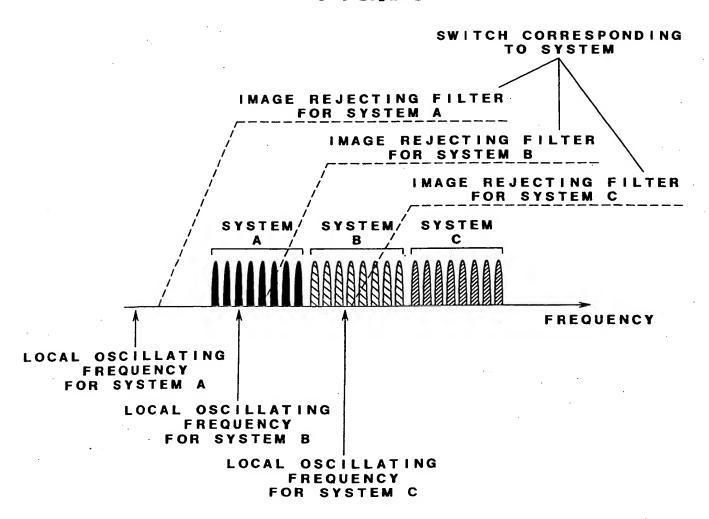


FIG.20



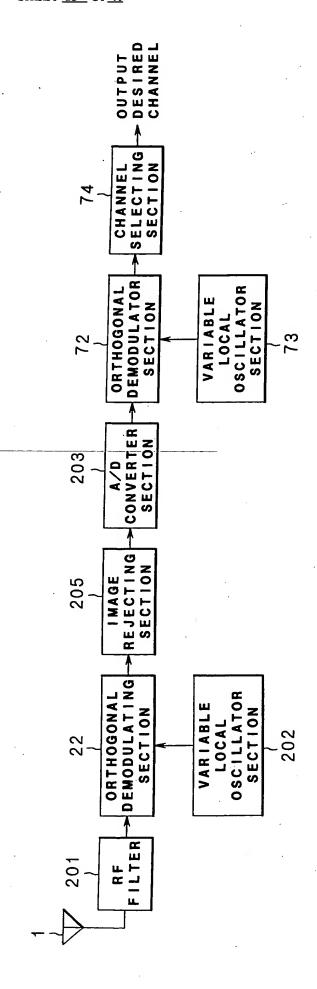
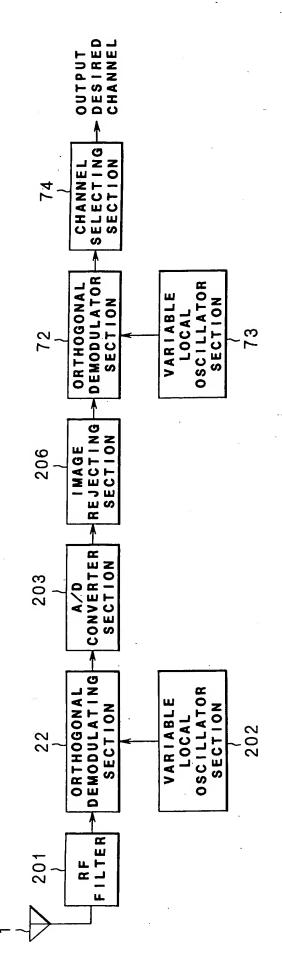
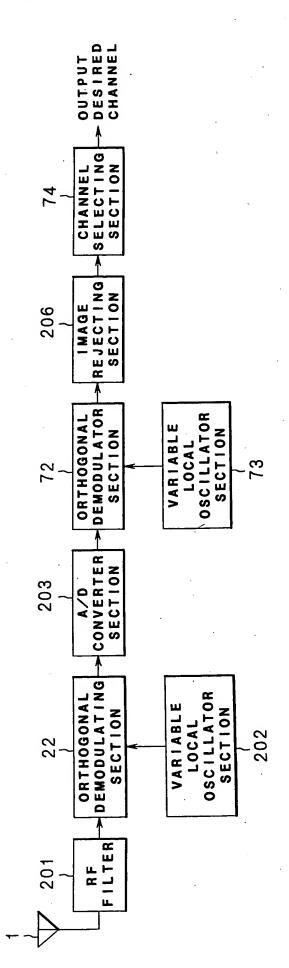


FIG.21

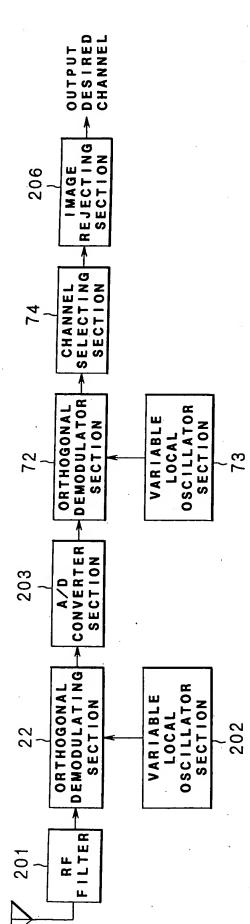




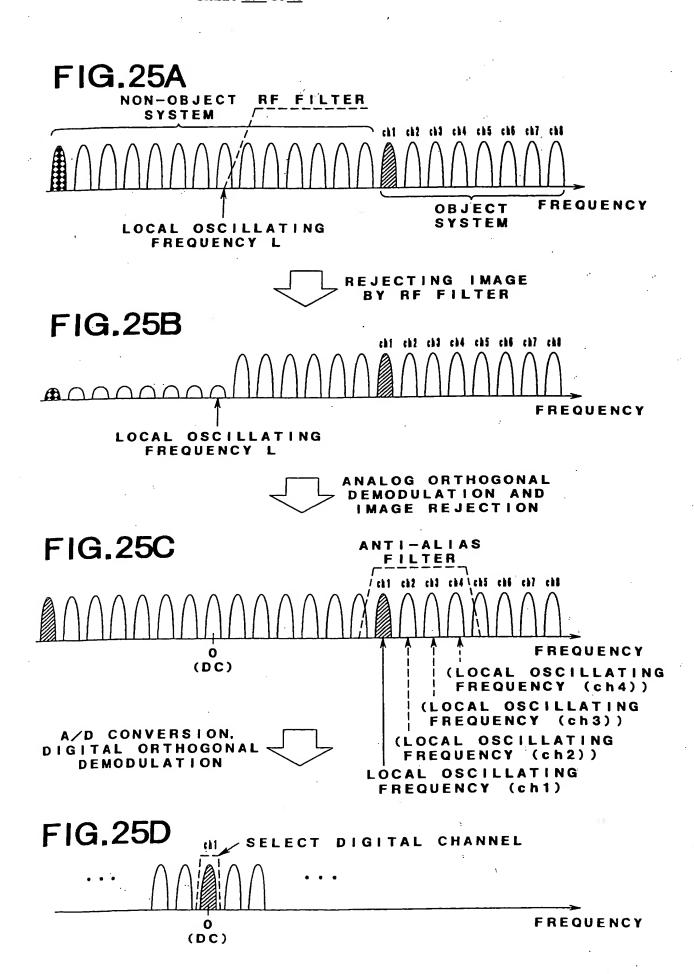




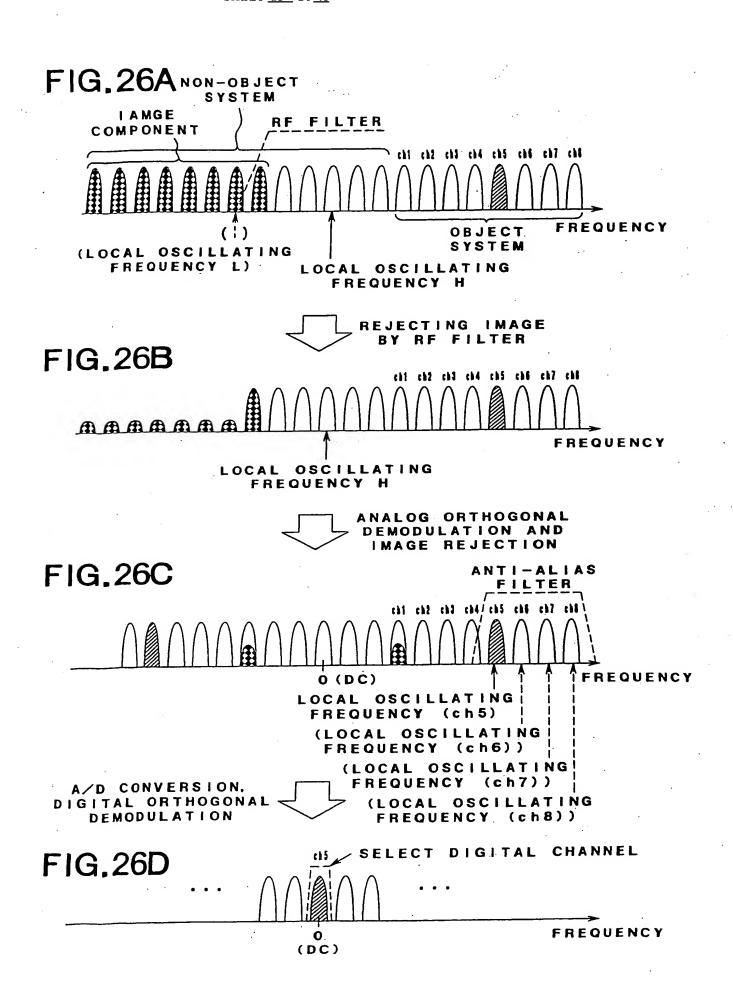




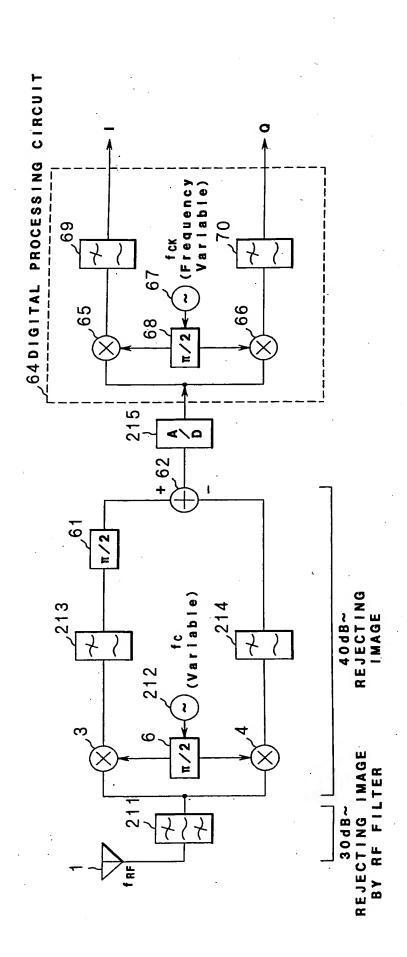
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 27 OF 41

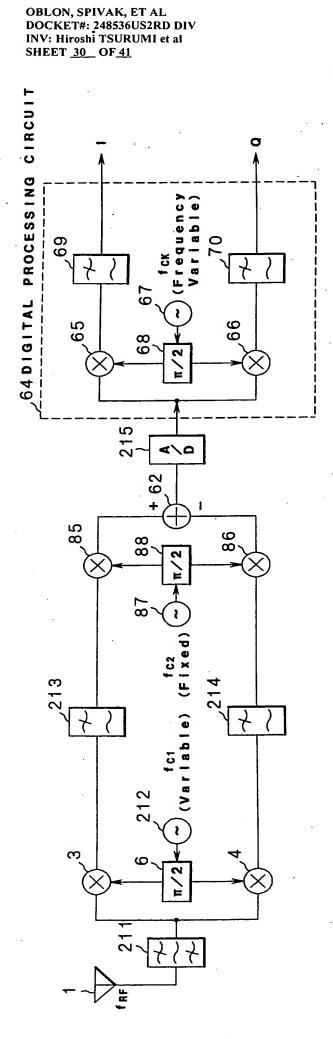


OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 28 OF 41









,64 DIGITAL PROCESSING CIRCUIT (Frequency Variable) 69 **6**7 99 65 89 + 94 က္မ 216 A/O **4\0** 9

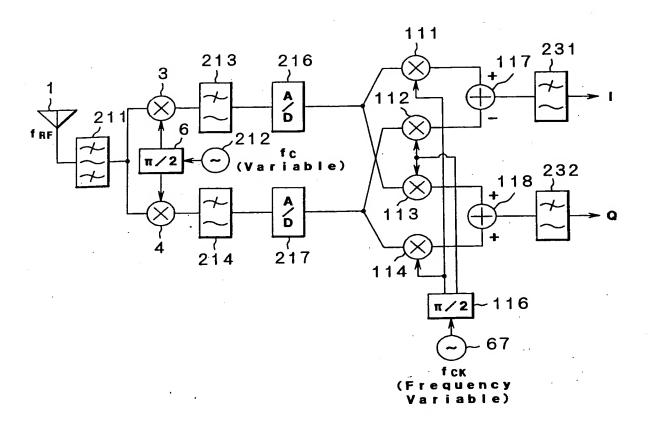
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV

INV: Hiroshi TSURUMI et al SHEET 31 OF 41

INV: Hiroshi TSURUMI et al SHEET 32 OF 41 ,64 DIGITAL PROCESSING CIRCUIT 69 29 65 99 89 + 94 104 103 216 A/O

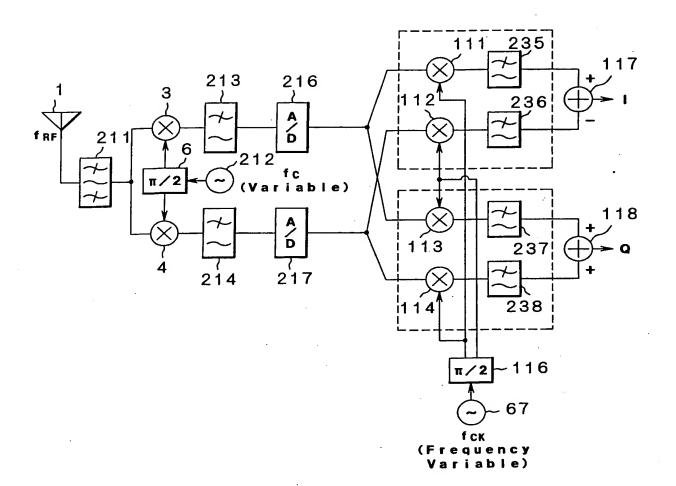
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 33 OF 41

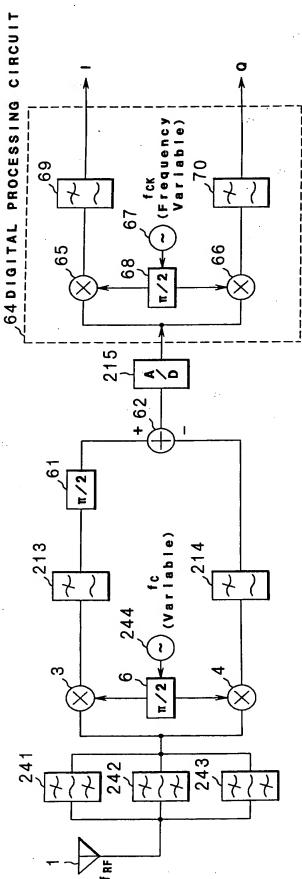
FIG.31



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FIG.32



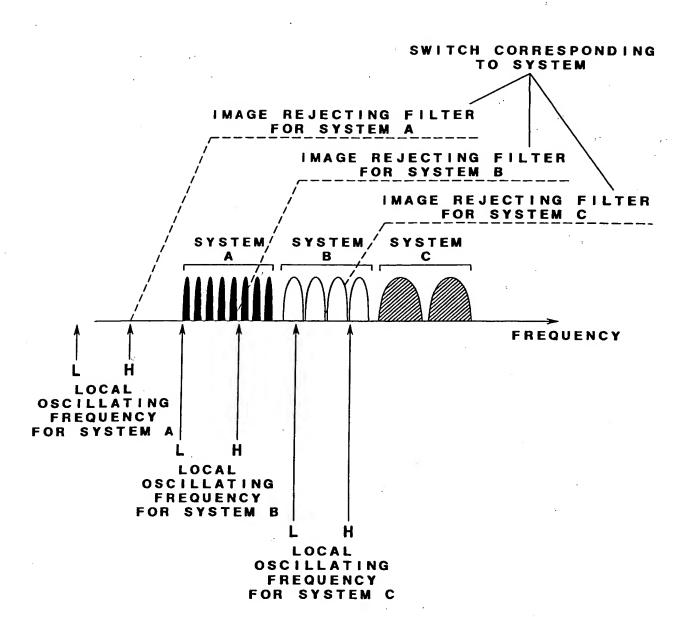


OBLON, SPIVAK, ET AL DOÇKET#: 248536US2RD DIV

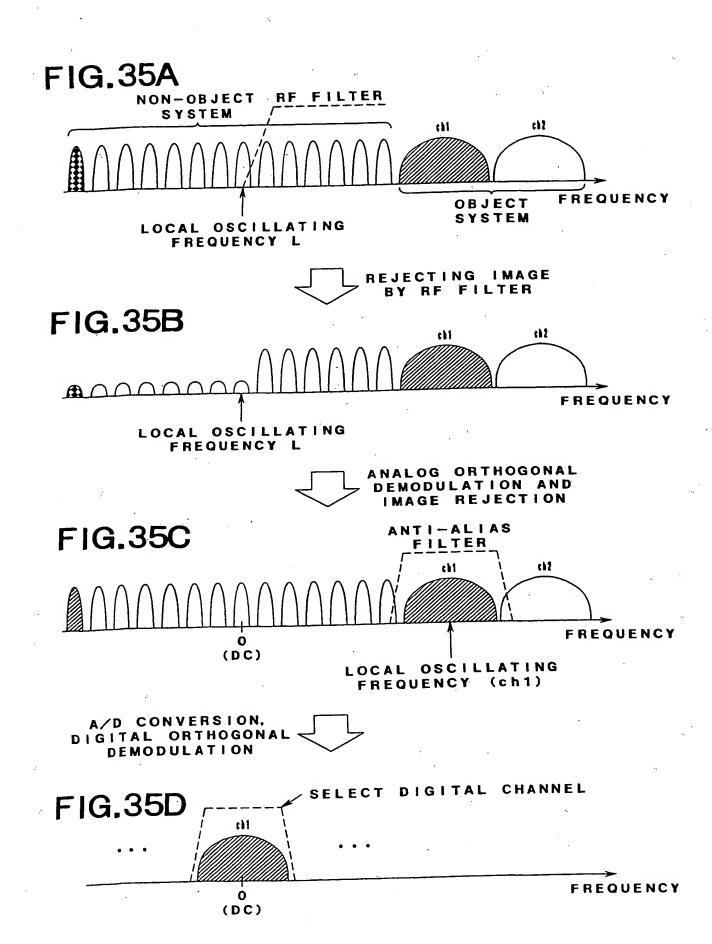
FIG.33

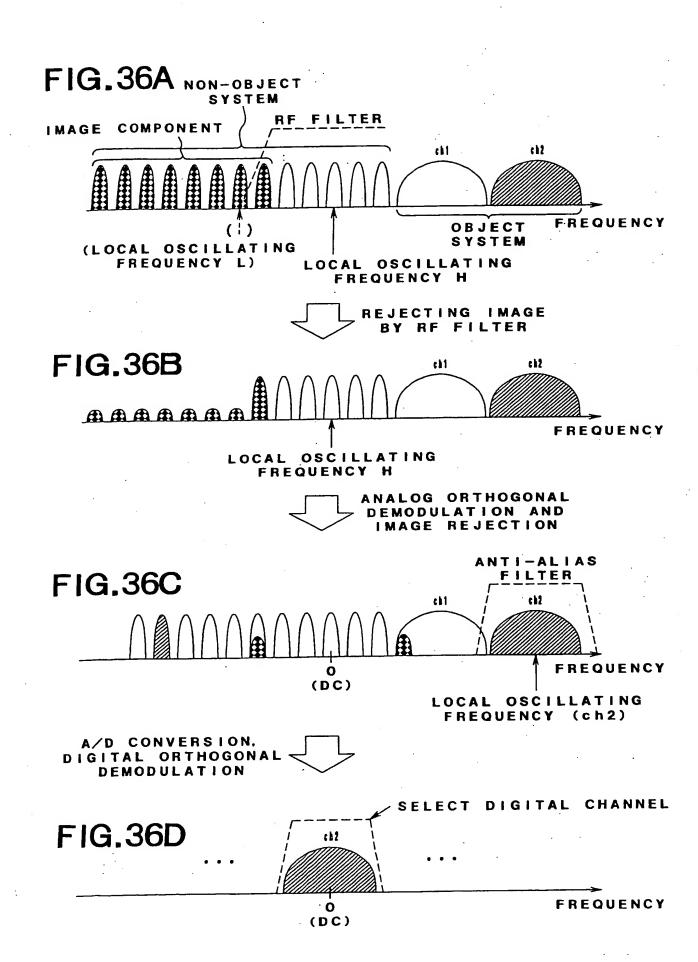
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 36 OF 41

FIG.34

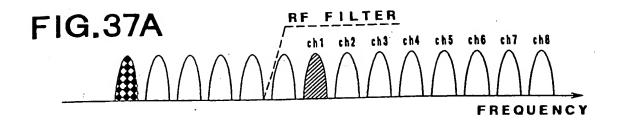


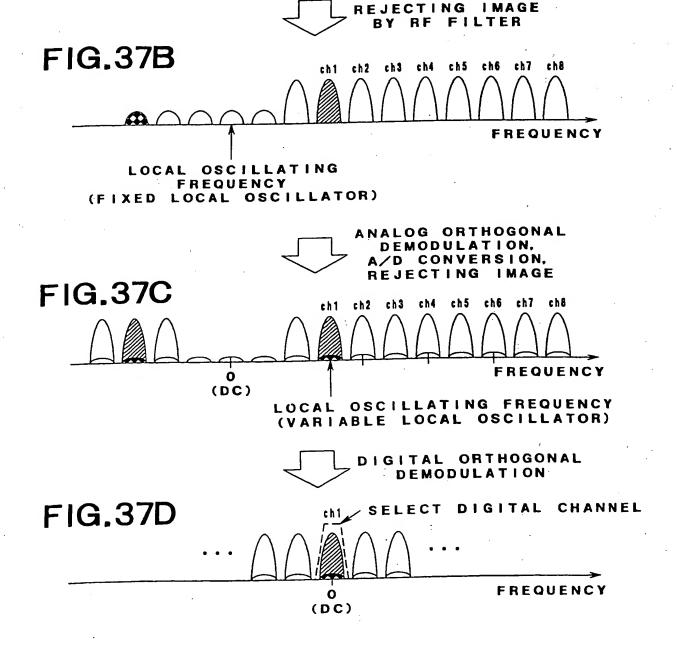
OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 37 OF 41



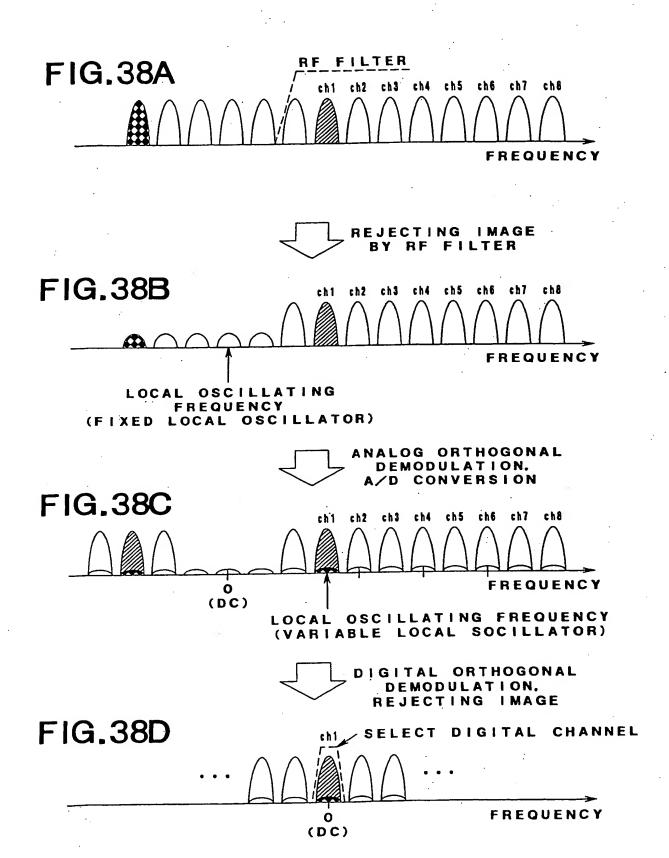


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OBLON, SPIVAK, ET AL DOCKET#: 248536US2RD DIV INV: Hiroshi TSURUMI et al SHEET 40 OF 41



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